

FIG. 1

	Bridge Module <u>42G</u>	
	Third Party Module <u>42E</u>	
ne <u>40</u>	I/O Module 42E	
One Ring Transit Time 40	CPU Module 3 <u>42D</u>	FIG. 2
One Ri	CPU Module 2 <u>42C</u>	
	CPU Module 1 <u>42B</u>	
	CPU Module 0 <u>42A</u>	Time Slot

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Ser.	: S
Err.	Se
BF	89
Ret.	99
IMB	64
Far	<u>85</u>
Return	Addr 60
CTL	28
Own	26
Address/Data	54 54
Address/Data	- 1610 - 52 

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Operation Type	Address/Data Field 1	Address/Data Field 2
Read Near Request	Address 1	Address 2
Read Near Reply	Data 1	Data 2
Read Far Request	Address	Return Address
Read Far Reply	Return Address	Data
Write	Address	Data
Atomic Request	Address	Data
Atomic Reply	Address	Data
Read Chain - Frame 1	Address	Size and Return Address
Read Chain - Other Frames	Data	Data
Write Chain - Frame 1	Target Address	Source Address
Write Chain - Other Frames	Data	Data
82	84 —	86 —
	<b>5</b> 10 4	86

FIG. 4

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Control Field	<u>Transaction</u>
000	Null
001	Read
010	Write
011	Atomic
100	End Read Chain
101	Read Chain
110	Write Chain
111	End Write Chain



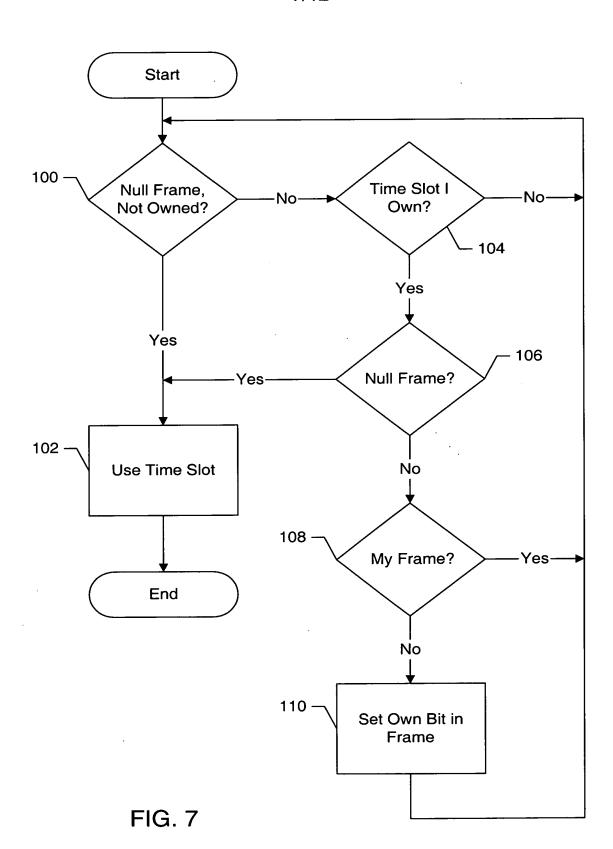
FIG. 5

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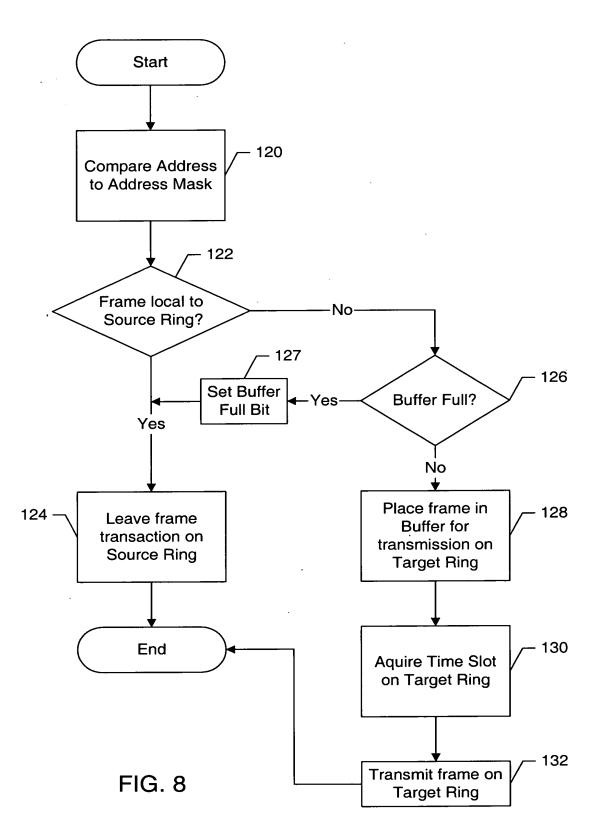
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Internal Ring Address	<u>86</u>
Ring	<u>96</u>
Ring Address	94

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FIG. 9

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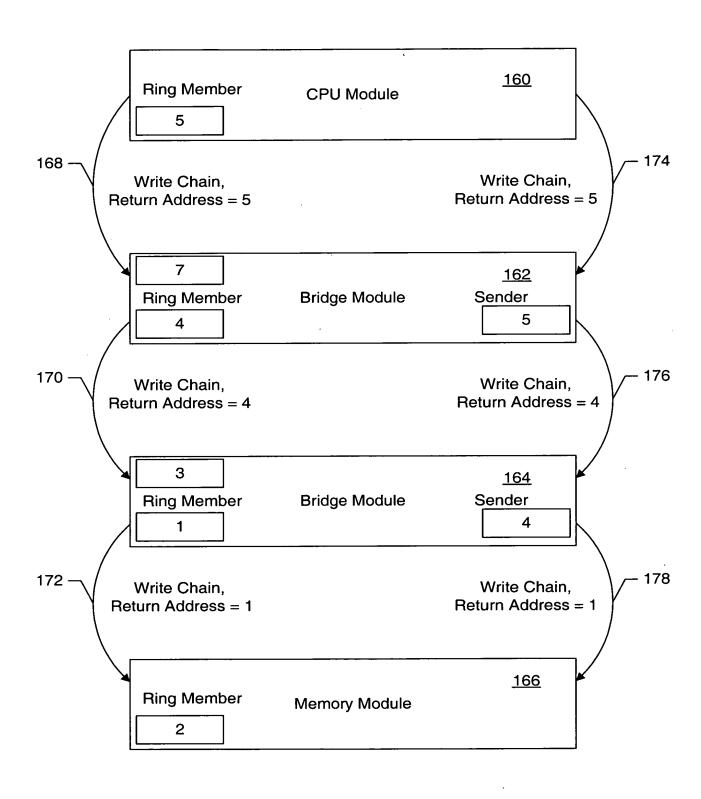


FIG. 10



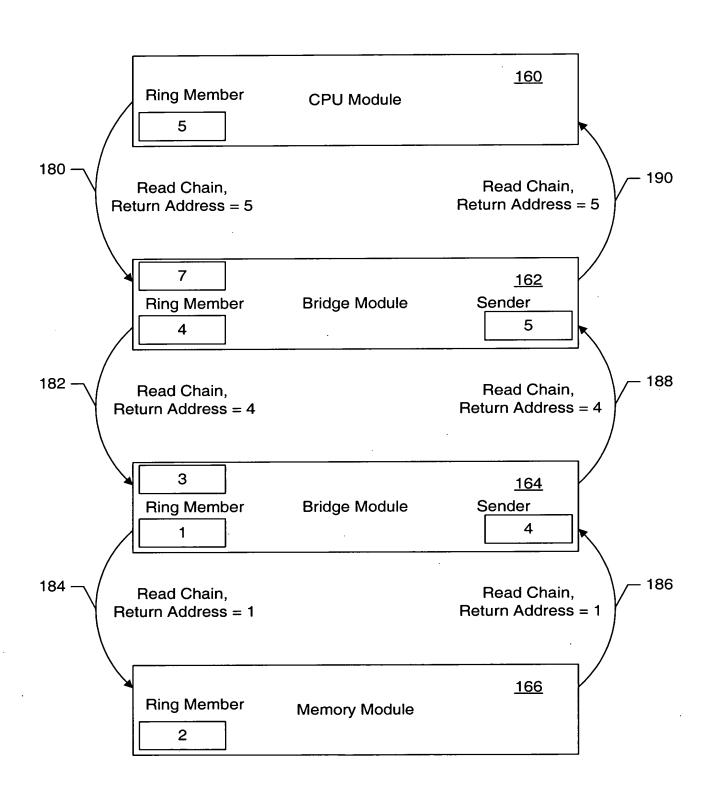


Fig. 11

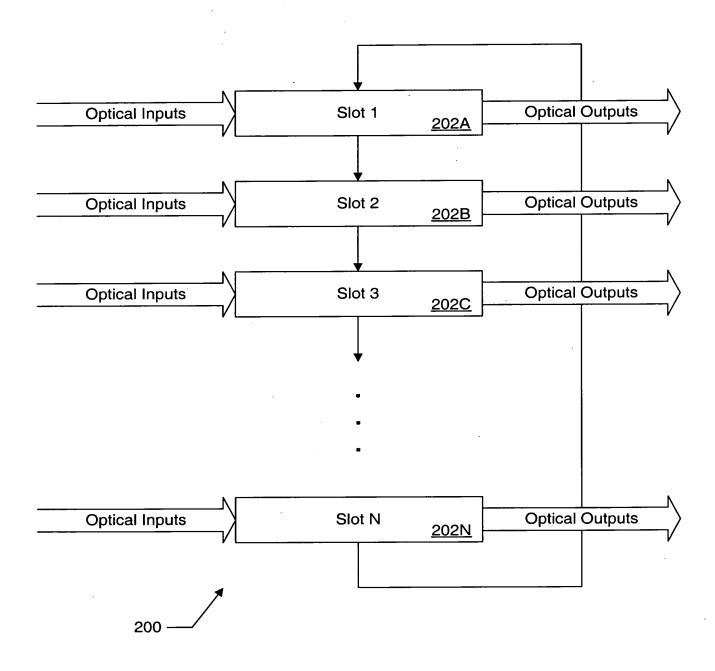


FIG. 12

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